

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re patent application of

K. Yamada et al.

Serial No.: Unassigned

Group Art Unit: Unassigned

Filed: Concurrently

Examiner: Unknown

For: SEMICONDUCTOR DEVICE TESTER

Assistant Commissioner of Patents  
Washington, D.C. 20231

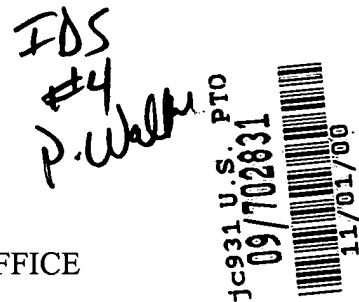
**INFORMATION DISCLOSURE STATEMENT**

Sir:

Under the provisions of 37 CFR §1.97 through §1.99 and pursuant to applicant's duty of disclosure under 37 CFR §1.56, applicant respectfully brings the following documents listed on the attached form PTO-1449, to the attention of the Examiner in charge of the above-identified application. A copy of the listed documents is provided herewith for the convenience of the Examiner. In compliance with the concise explanation requirement under 37 CFR §1.98(a)(3), the relevance of the related documents is discussed on pages 4, 5, 6 and 13 of the subject application.

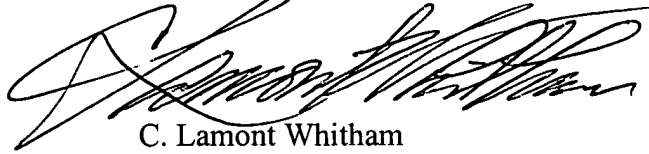
This citation does not constitute an admission that the references are relevant or material to the claims. They are only cited as constituting related art of which the applicant is aware.

It is respectfully requested that the listed references be considered by the Examiner and formally made of record in this application.



Please charge any deficiencies in fees and credit any overpayment of fees to  
attorney's deposit account no. 23-1951

Respectfully submitted,

A handwritten signature in black ink, appearing to read "C. Lamont Whitham", written in a cursive style.

C. Lamont Whitham

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